

2N7002KW



N-Channel Enhancement Mode Trench Power MOSFET

Voltage: 60 Volts

Current: 0.34 Amperes

Package: SOT-323

Features

- NH'S Advanced Trench Technology
- Low Rds(on) For Low On-State Loss
- High EAS For High Reliability
- Excellent Qg*Rds(on) Product(FOM)
- Built-in ESD Protected

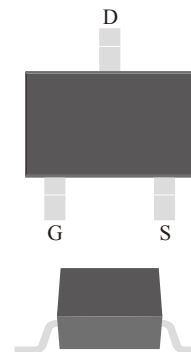
Typical Applications

- DC-DC Converter
- Battery Management System(BMS)
- Printed Circuit Board For Control Circuit
- Uninterruptible Power Supplies(UPS)

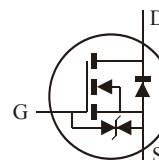
Product Summary

VDS Min.@Tj	60	V
ID Min.@Ta	0.34	A
RDS(ON)(TYP)@10V	1800	mΩ

Diagram:



Polarity:



***100% UIS TESTED**

***100% ΔVDS TESTED**

Absolute Maximum Ratings (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current (Note 1)	Ta= 25 °C	I _D	0.34	A
	Ta= 100 °C		0.22	
Drain Current-Pulse (Note 1)	Tj< 150 °C	I _{DM}	1	A
Maximum Power Dissipation Power	Ta= 25 °C	P _D	200	mW
Dissipation Derating Factor Above 25°C	Ta= 100 °C		80	
Derating Factor		D _F	1.60	mW/°C

Thermal Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Junction Temperature		T _J	-55 to 150	°C
Storage Temperature Range		T _{STD}	-55 to 150	°C
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25°C	R _{θJA}	625	°C/W

Notes: 1.Pulse Width Limited By Max. Junction Temperature. (See Fig.13)

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Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static Off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	BV _{DSS}	60	--	--	V
Bvdss Temperature Coefficient	ID=250uA,Reference25℃	ΔBV _{DSS} /ΔT _J	--	0.07	--	V/℃
Drain-Source Leakage Current	VDS= 60 V,VGS=0V	I _{DSS}	--	--	1	uA
Gate-Body Leakage Current	VGS= ±20 V,VDS=0V	I _{GSS}	--	--	±10	uA
Static On Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	V _{GS(TH)}	1	1.75	2.5	V
Drain-Source On Resistance	ID= 0.2 A,VGS= 10.0 V	R _{DS(ON)}	--	1800	2500	mΩ
	ID= 0.2 A,VGS= 5.0 V		--	2100	3200	
Dynamic Characteristics						
Input Capacitance	VDS= 30 V VGS= 0 V F= 1 MHZ	C _{iss}	--	40	--	pF
Output Capacitance		C _{oss}	--	30	--	pF
Reverse Transfer Capacitance		C _{rss}	--	10	--	pF
Switching Paramters With Inductive Load(Test Circuit & Waveform See Fig.14)						
Turn-On Delay Time	VDS= 30 V VGS= 10.0 V RG= 10.0 Ω L= 0.0 0	t _{d(on)}	--	10	--	ns
Turn-Off Delay Time		t _{d(off)}	--	15	--	ns
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.17)						
Max. Diode Forward Cuurent		I _S	--	--	0.34	A
Max. Pulsed Forward Cuurent		I _{SM}	--	--	1.36	A
Diode Forward Voltage	ID= 0.17 A,VGS=0V	V _{SD}	--	0.8	1.5	V
Reverse Recovery Time	IS= 0.17 A,di/dt= 100 A/us	t _{rr}	--	30	--	ns
Reverse Recovery Charge		VGS= 0.0 V	Q _{rr}	--	30	--

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Typical Characteristics Curves

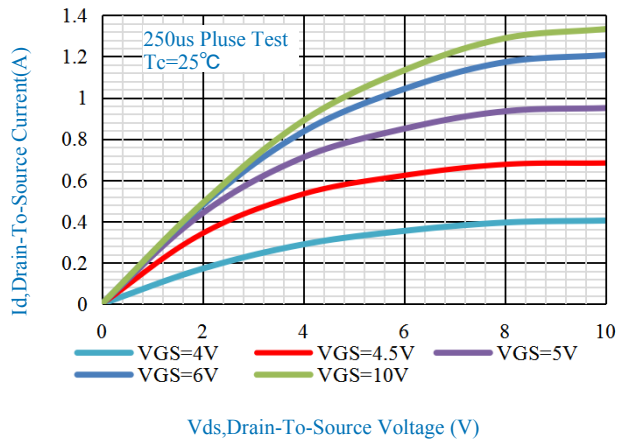


Fig.1-Output Characteristics

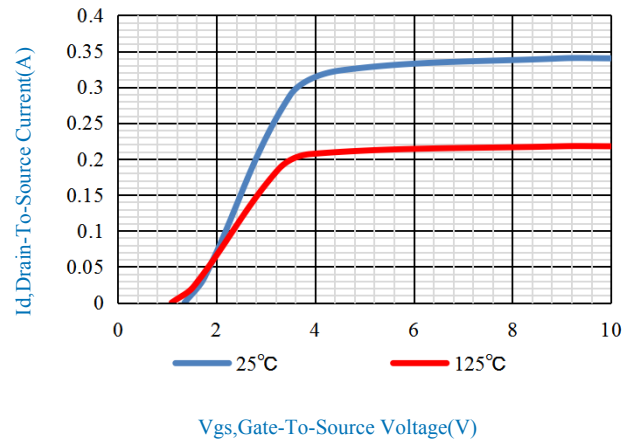


Fig.2-Transfer Characteristics

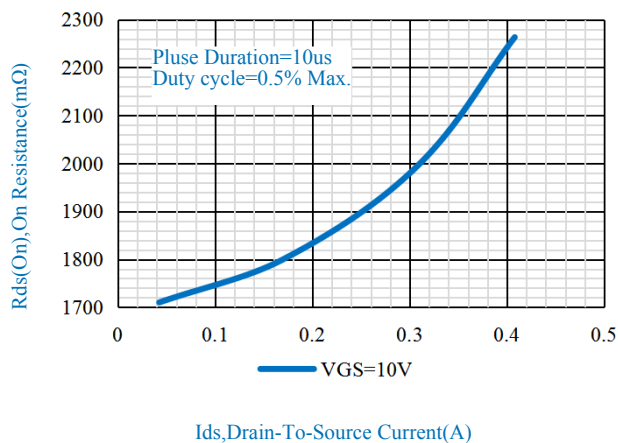


Fig.3-On Resistance Vs. Drain Current

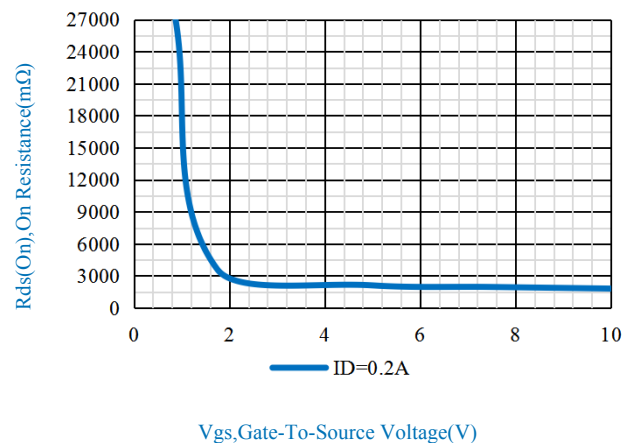


Fig.4-On Resistance Vs. Gate Source Voltage

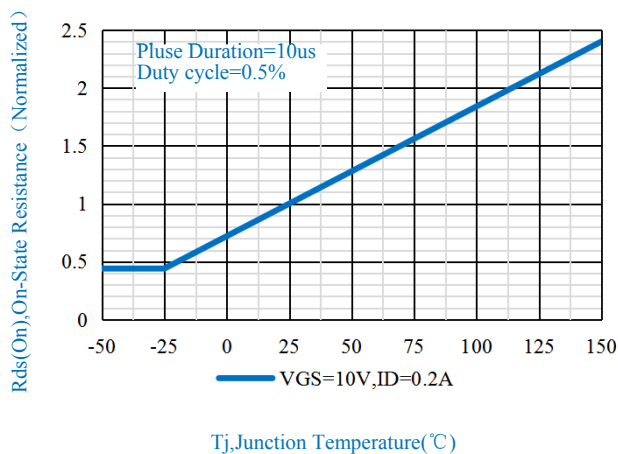


Fig.5-On Resistance Vs. Junction Temperature

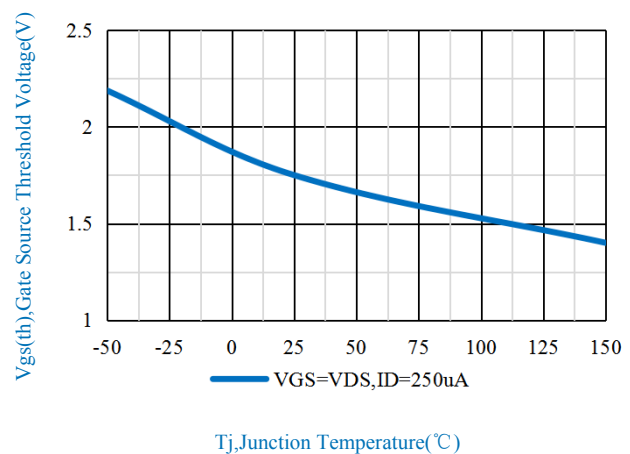


Fig.6-Gate Source Threshold Voltage Vs. Junction Temperature

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Typical Characteristics Curves

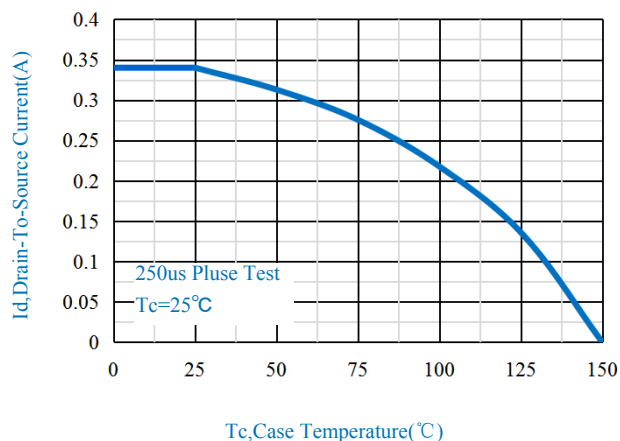


Fig.7-Maximum Continuous Drain Current Vs. Case Temperature

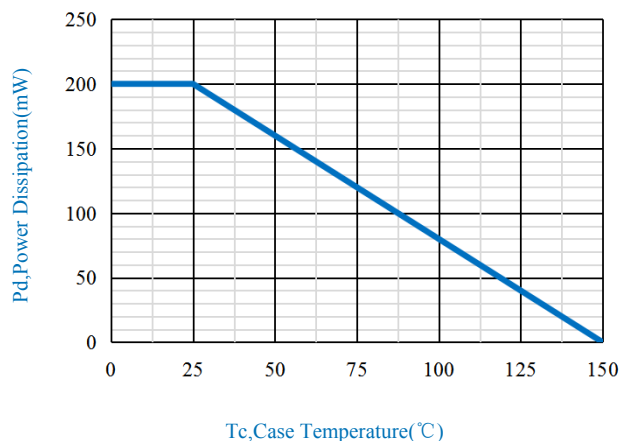


Fig.8-Maximum Power Dissipation Vs. Case Temperature

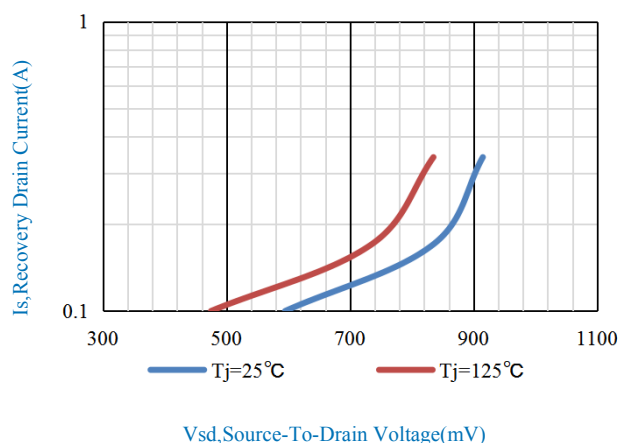


Fig.9-Source-To-Drain Diode Forward Voltage

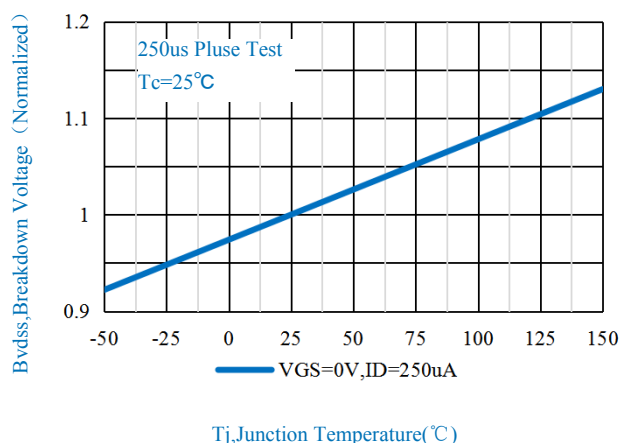


Fig.10-Breakdown Voltage Vs. Junction Temperature

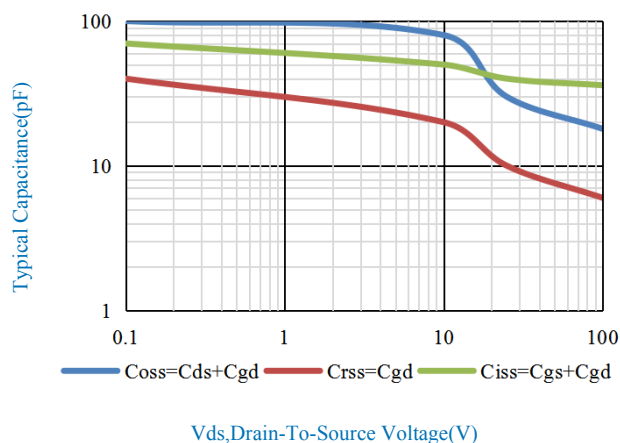


Fig.11-Typical Capacitance Vs. Drain-To-Source Voltage

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Typical Characteristics Curves

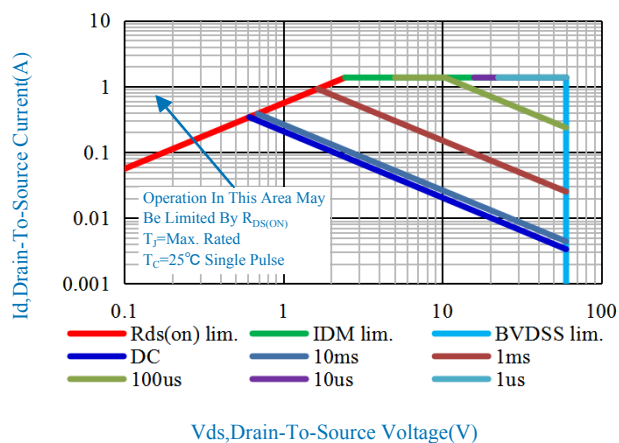


Fig.12-Maximum Safe Operating Area(SOA)

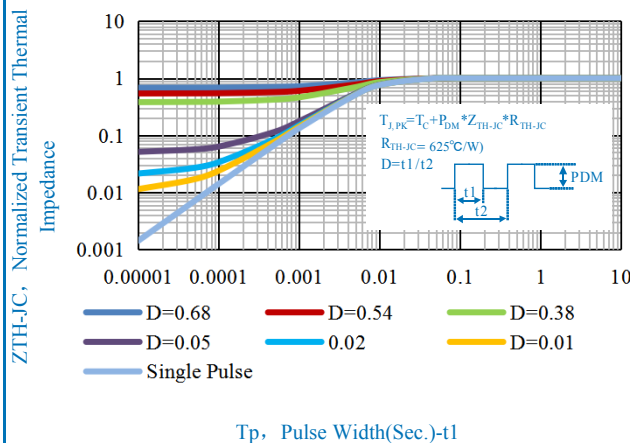


Fig.13- Normalized Maximum Transient Thermal Impedance Vs. Pulse Width

Inductive Load Test Circuit & Waveform

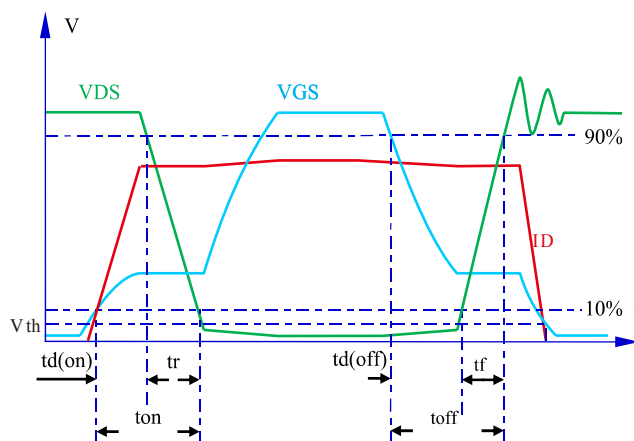
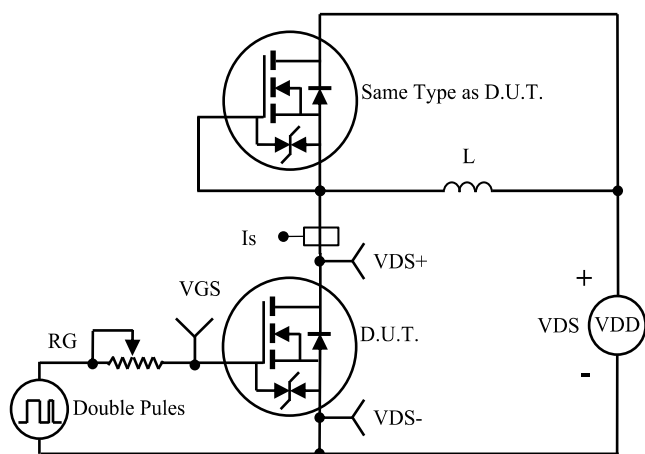


Fig.14- Inductive Switching Test Circuit & Waveform

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Test Circuit & Waveform

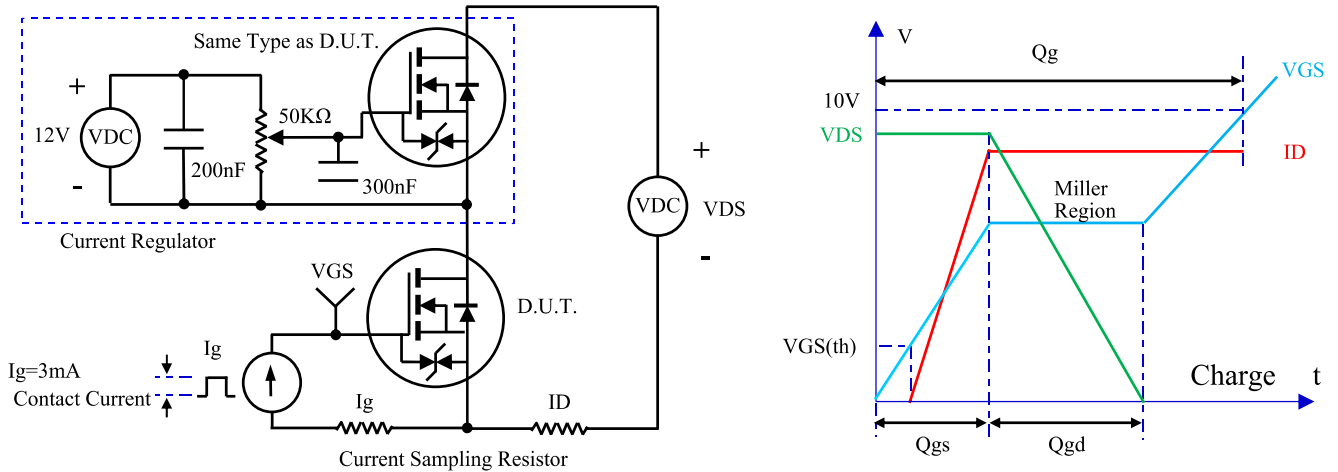


Fig.15- Gate Charge Test Circuit & Waveform

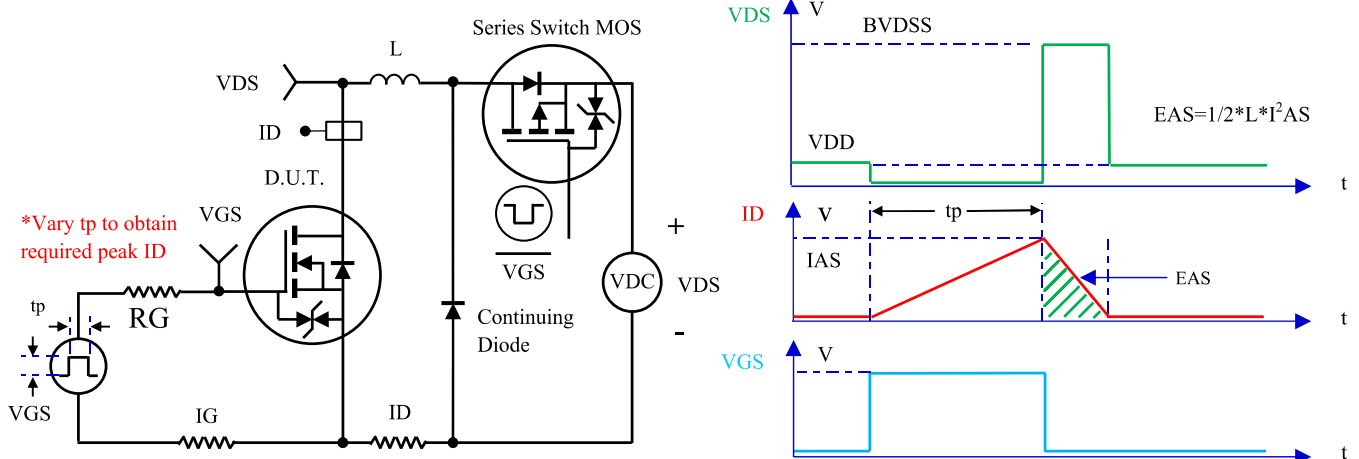


Fig.16- Unclamped Inductive Switching (UIS) Test Circuit & Waveform

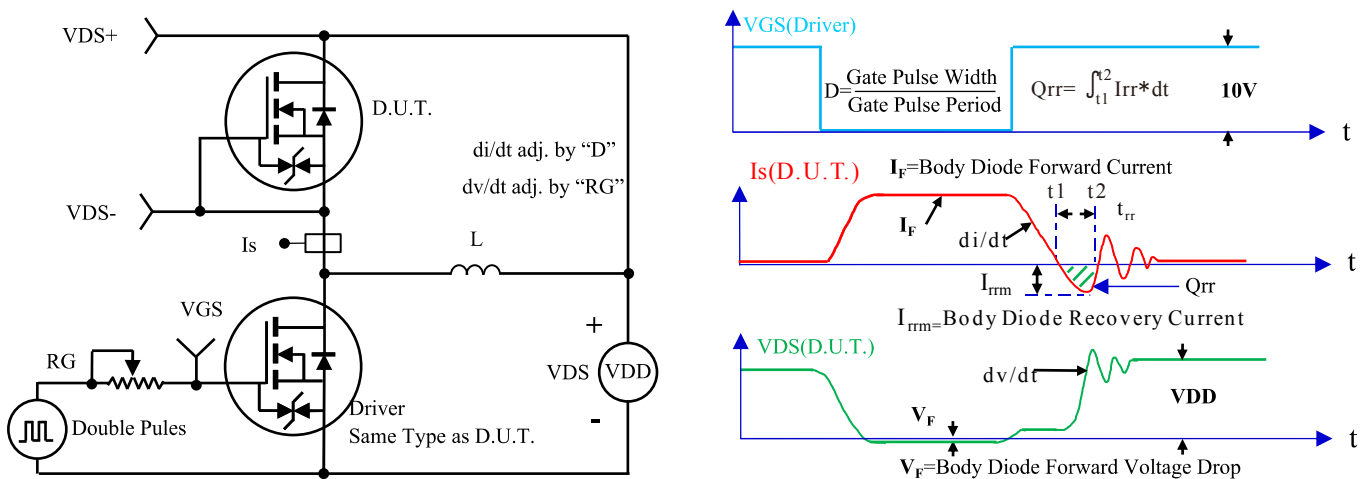


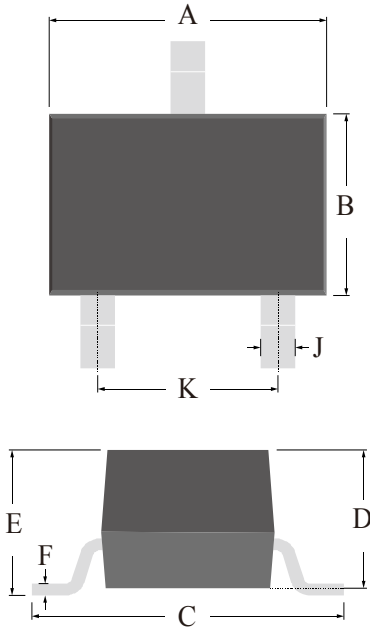
Fig.17- Diode Recovery Test Circuit & Waveform

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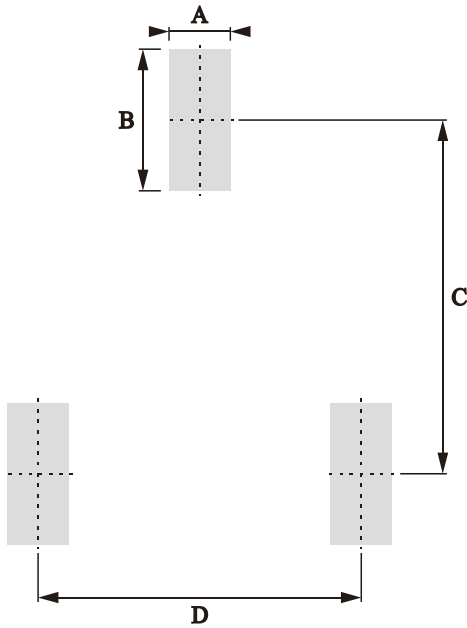
OUTLINE DRAWINGS



SOT-323

OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.80	-	2.20	0.0709	-	0.0866
B	1.10	-	1.40	0.0433	-	0.0551
C	2.05	-	2.45	0.0807	-	0.0965
D	0.80	-	1.10	0.0315	-	0.0433
E	0.85	-	1.15	0.0335	-	0.0453
F	0.08	-	0.18	0.0031	-	0.0071
J	0.15	-	0.35	0.0059	-	0.0138
K	1.10	-	1.50	0.0433	-	0.0591

RECOMMEDND LAYOUT DRAWINGS



SOT-323

OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	0.60	-	-	0.0000	-
B	-	0.80	-	-	0.0315	-
C	-	2.00	-	-	0.0787	-
D	-	1.40	-	-	0.0551	-

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MARKING INFORMATION

MARKING INSTRUCTIONS



72K=Mark

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
SOT-323	P1	0.0291	7" Reel	3000	45000	180000

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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow

	Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
	Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
	Preheat-Temperature Min (T _S min) -Temperature Max (T _S max) -Time(t _s min to t _s max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
	Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
	Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
	Time within 5°C of actual peak temperature (t _p)	10-30 seconds	20-40 seconds
	Ramp down rate	6°C/second max.	6°C/second max.
	Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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Specification Revision History

Rev.	Date	Changed Items	Pre-Changed Content	Changed Content
A/1	2014-10-20	First Issue		
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