

NPS4N70ES

N-Channel Enhancement Mode Power MOSFET



Voltage: 700 Volts

Current: 4.0 Ampers

Package: TO-251S

Features

- NH'S Advanced Planar DMOS Technology
- Low Rds(on) For Low On-State Loss
- High EAS For High Reliability
- Qualified According To JEDEC Criteria

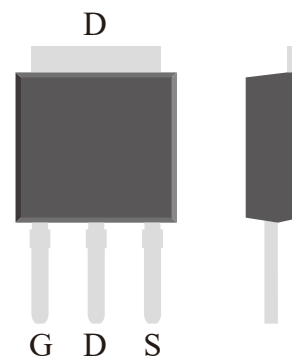
Typical Applications

- AC/DC Converter
- Adaptor And Charger
- PC Power
- LED Drives And LED Lighting

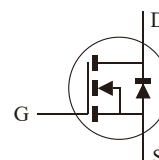
Product Summary

VDS Min.@Tj	700	V
Id Min.@Ta	4.0	A
RDS(ON)(TYP)@10V	2.50	Ω

Diagram:



Polarity:



***100% UIS TESTED**

***100% ΔVDS TESTED**

Absolute Maximum Ratings (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V _{DS}	700	V
Gate-Source Voltage		V _{GS}	±30	V
Continuous Drain Current (Note 1)	Ta= 25 ℃	I _D	4.0	A
	Ta= 100 ℃		2.6	
Drain Current-Pulse (Note 1)	Tj< 150 ℃	I _{DM}	16	A
Maximum Power Dissipation Power Dissipation Derating Factor Above 25℃	Ta= 25 ℃	P _D	75	W
	Ta= 100 ℃		30	
Derating Factor		D _F	0.60	W/℃
Avalanche Current,Single Pulse (Note 1)	L= 10.0 mH	I _{AS}	5.5	A
Single Pulse Avalanche Energy (Note 1) Test Circuit & Waveform See Fig.16	L= 10.0 mH IAS= 5.5 A, RG= 10.0 Ω Starting Tj=25 ℃, VG= 10.0 V	E _{AS}	150	mJ

Thermal Characteristics (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Junction Temperature		T _J	-55 to 150	℃
Storage Temperature Range		T _{STD}	-55 to 150	℃
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25℃	R _{θJA}	62.5	℃/W
Thermal Resistance Junction-Case With Steady-State	Device Mounted On 75mm x 45mm x 2.5mm Alu. Heat.	R _{θJC}	1.67	

Notes: 1.Pulse Width Limited By Max. Junction Temperature. (See Fig. 13).

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Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static Off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	BV _{DSS}	700	--	--	V
Bvdss Temperature Coefficient	ID=250uA,Reference25℃	ΔBV _{DSS} /ΔT _J	--	0.83	--	V/℃
Drain-Source Leakage Current	VDS= 700 V,VGS=0V	I _{DSS}	--	--	10	uA
Gate-Body Leakage Current	VGS= ±30 V,VDS=0V	I _{GSS}	--	--	±100	nA
Forward Transconductance	ID= 2.0 A,VDS= 15 V	g _{fs}	--	3.5	--	S
Static On Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	V _{GS(TH)}	2.0	3.0	4.0	V
Drain-Source On Resistance	ID= 2.0 A,VGS= 10.0 V	R _{DS(ON)}	--	2.5	2.8	Ω
	ID= 2.0 A,VGS= 4.5 V		--	3.3	4.2	
Dynamic Characteristics						
Input Capacitance	VDS= 50 V	C _{iss}	--	500.0	--	pF
Output Capacitance	VGS= 0 V	C _{oss}	--	50.0	--	pF
Reverse Transfer Capacitance	F= 1 MHZ	C _{rss}	--	9.0	--	pF
Switching Paramters (Test Circuit & Waveform See Fig.14)						
Turn-On Delay Time	VDS= 350 V	t _{d(on)}	--	12.0	--	ns
Turn-On Rise Time	VGS= 10.0 V	t _r	--	15.0	--	ns
Turn-Off Delay Time	RG= 10.0 Ω	t _{d(off)}	--	45.0	--	ns
Turn-Off Rise Time		t _f	--	23.0	--	ns
Gate Charge Paramters (Test Circuit & Waveform See Fig.15)						
Total Gate Charge	VDS= 350 V	Q _g	--	15.0	--	nC
Gate-Source Charge	VGS= 10.0 V	Q _{gs}	--	3.0	--	nC
Gate-Drain Charge	ID= 2.0 A	Q _{gd}	--	7.0	--	nC
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.17)						
Max. Diode Forward Cuurent		I _S	--	--	4.0	A
Max. Pulsed Forward Cuurent		I _{SM}	--	--	16.0	A
Diode Forward Voltage	ID= 2.0 A,VGS=0V	V _{SD}	--	0.84	1.51	V
Reverse Recovery Time	IS= 2.0 A,di/dt= 100 A/us	t _{rr}	--	198.0	--	ns
Reverse Recovery Charge	VGS= 0.0 V	Q _{rr}	--	770.0	--	nC
Reverse Recovery Current		I _{RRM}	--	7.8	--	A

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Typical Characteristics Curves

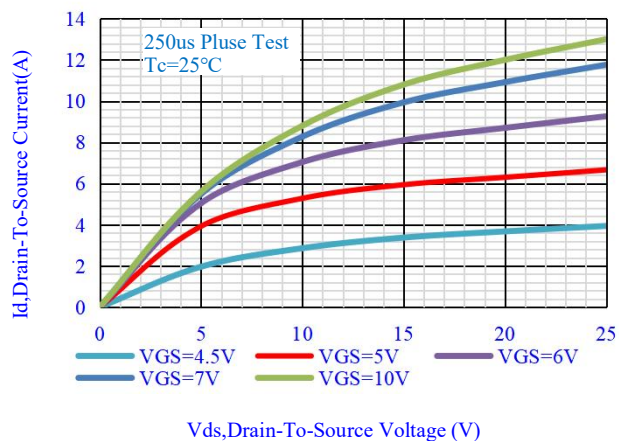


Fig.1-Output Characteristics

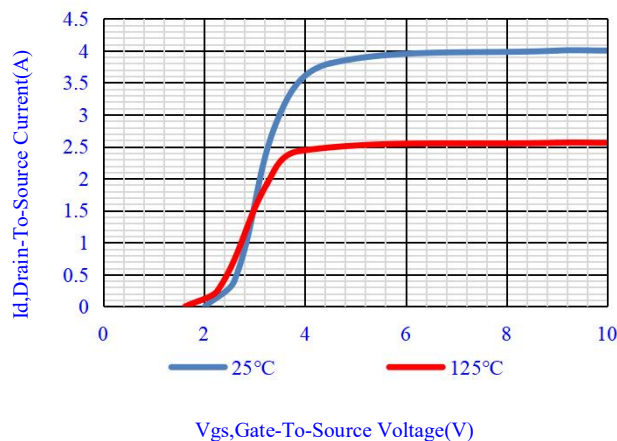


Fig.2-Transfer Characteristics

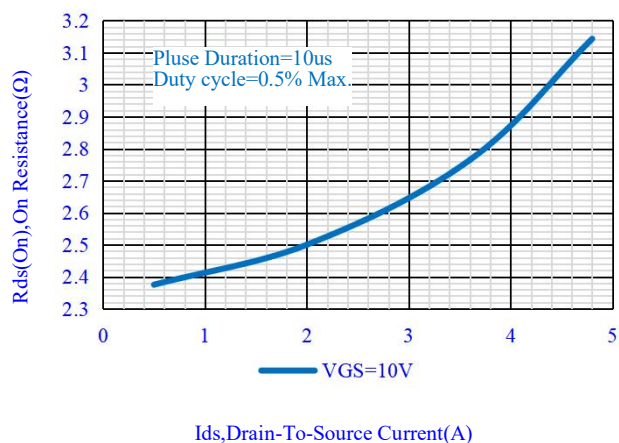


Fig.3-On Resistance Vs. Drain Current

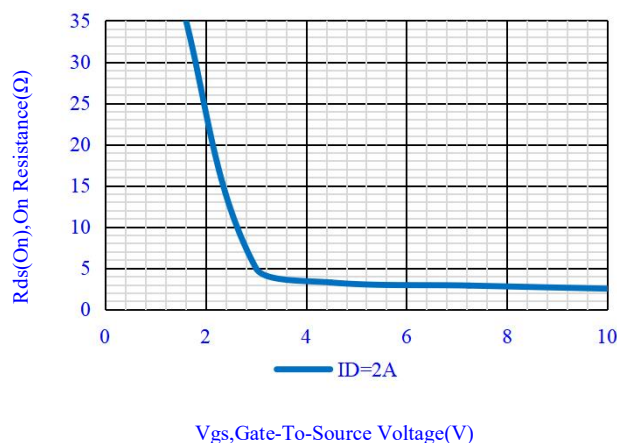


Fig.4-On Resistance Vs. Gate Source Voltage

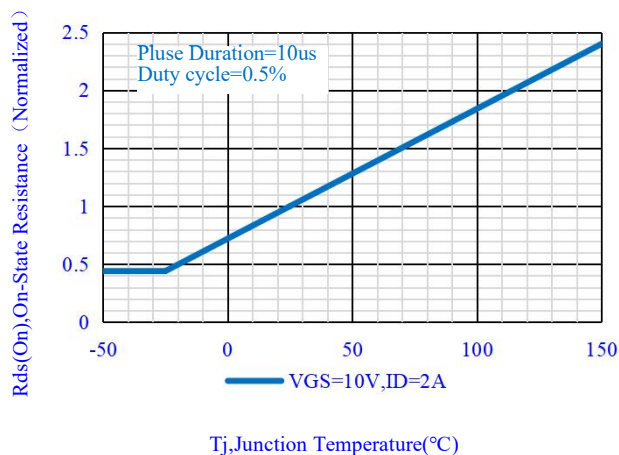


Fig.5-On Resistance Vs. Junction Temperature

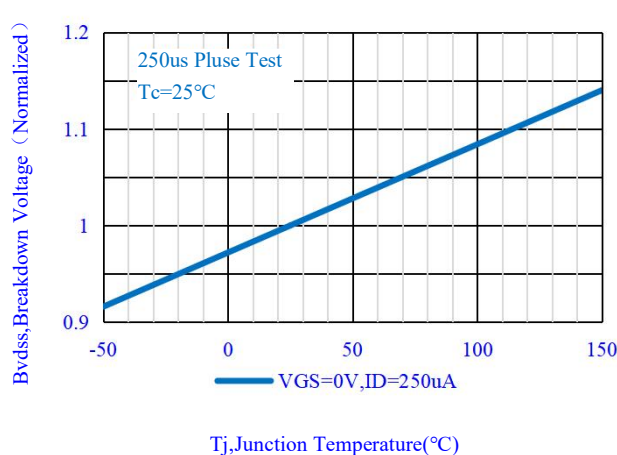


Fig.6-Breakdown Voltage Vs. Junction Temperature

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Typical Characteristics Curves

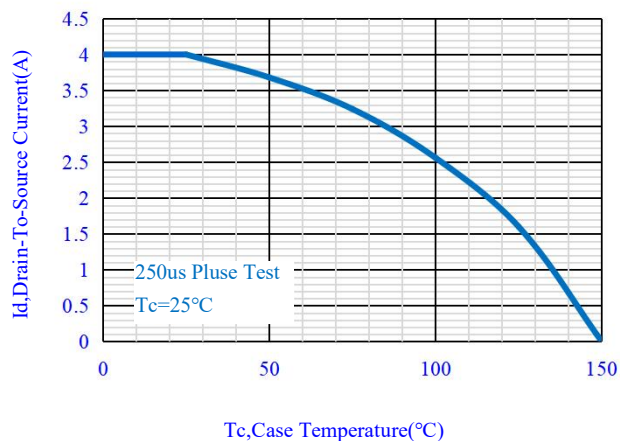


Fig.7-Maximum Continuous Drain Current Vs. Case Temperature

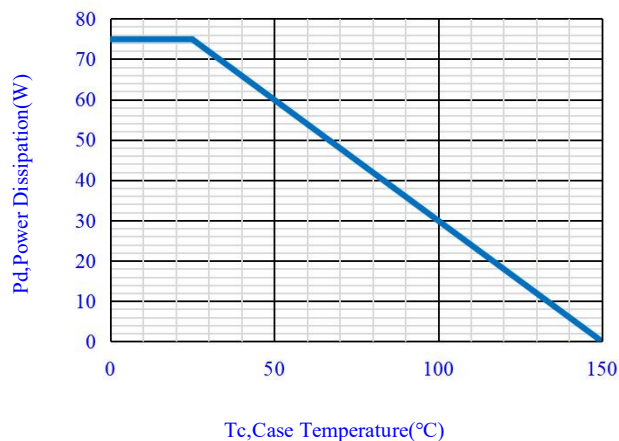


Fig.8-Maximum Power Dissipation Vs. Case Temperature

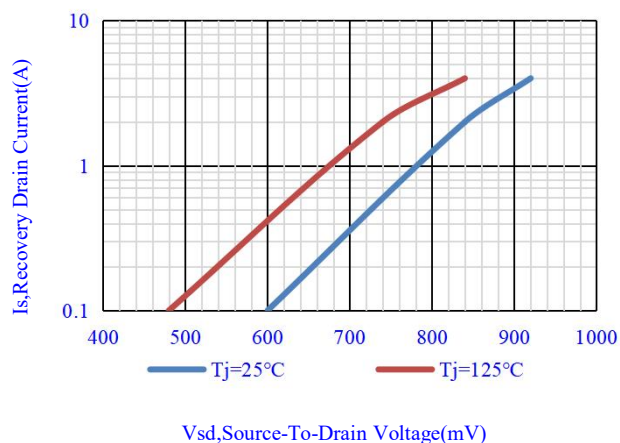


Fig.9-Source-To-Drain Diode Forward Voltage

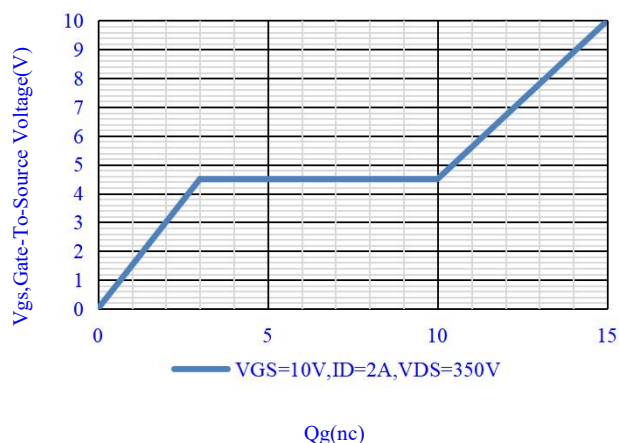


Fig.10-Gate Charge Waveform

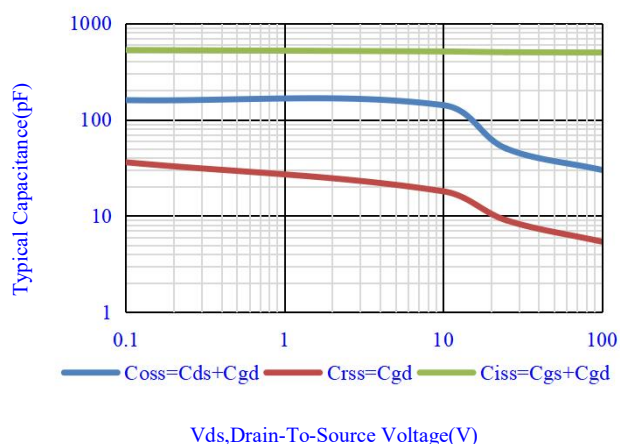


Fig.11-Typical Capacitance Vs. Drain-To-Source Voltage

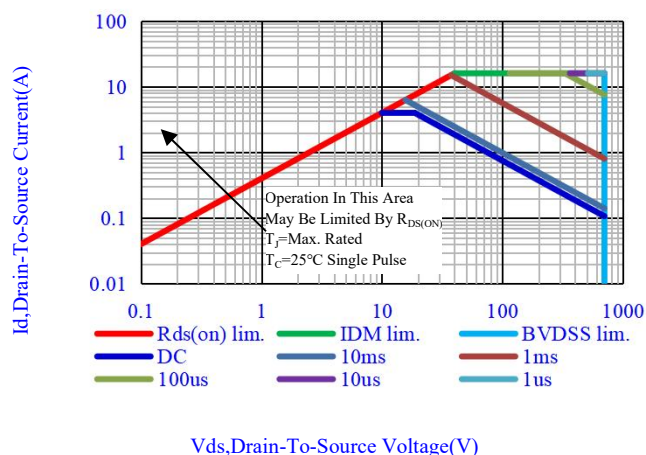


Fig.12-Maximum Safe Operating Area(SOA)

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Typical Characteristics Curves

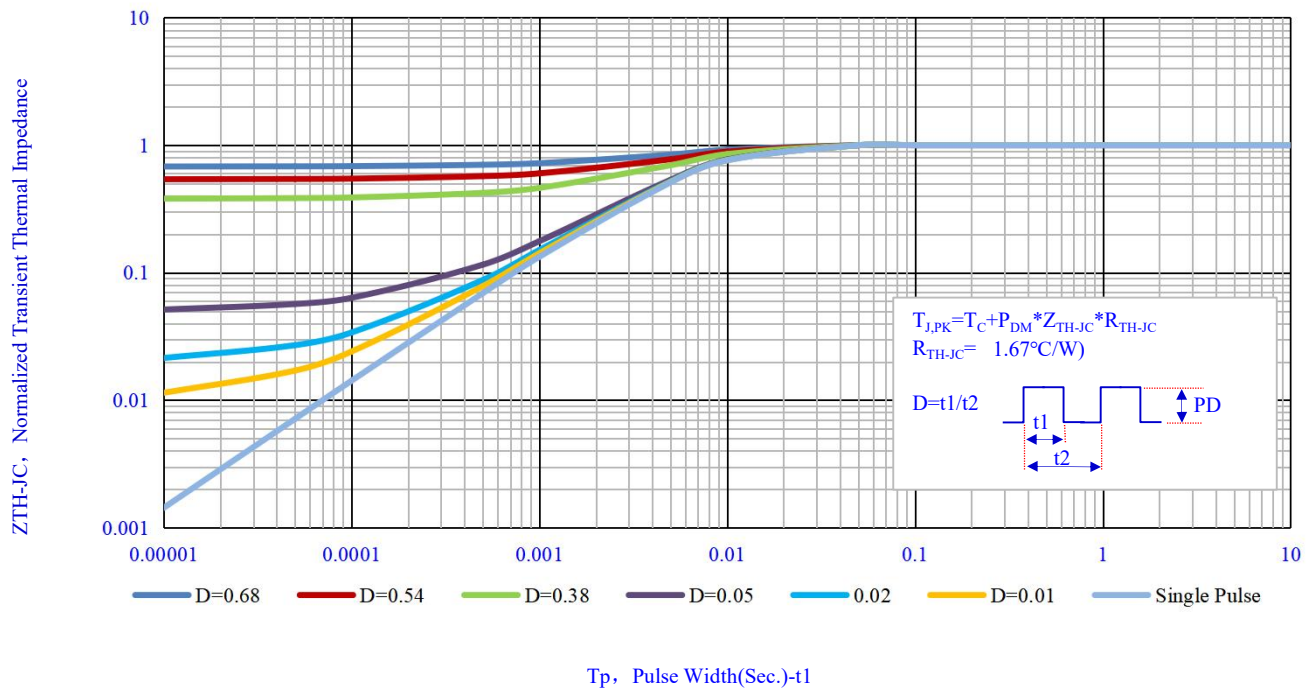


Fig.13- Normalized Maximum Transient Thermal Impedance Vs.Pulse Width

Test Circuit & Waveform

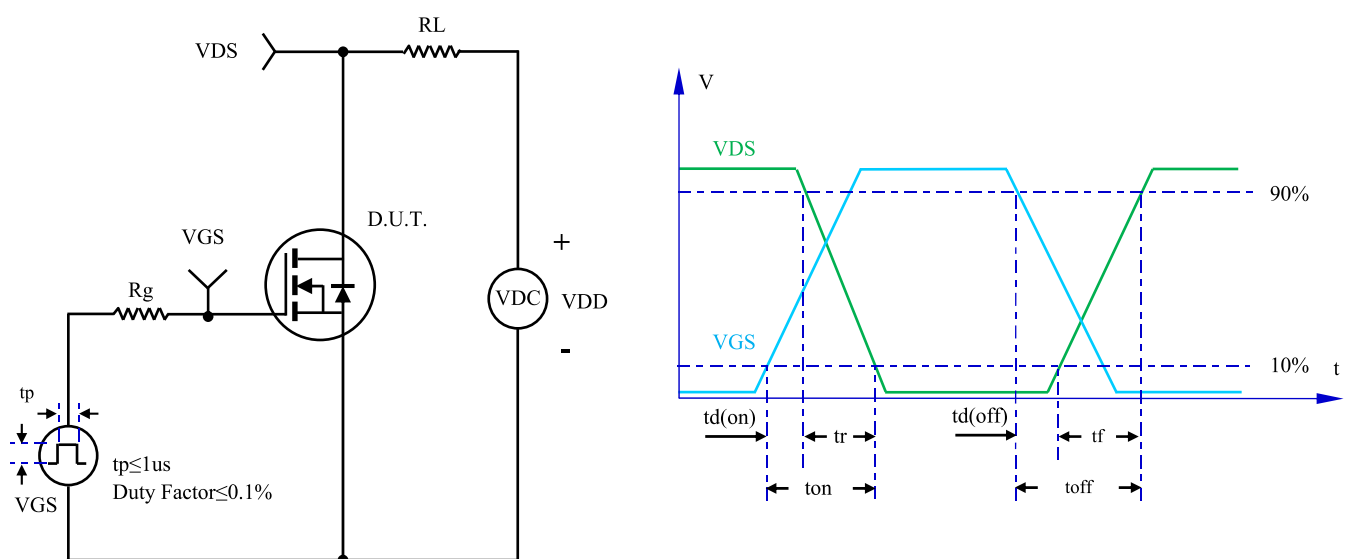


Fig.14- Resistive Switching Test Circuit & Waveform

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Test Circuit & Waveform

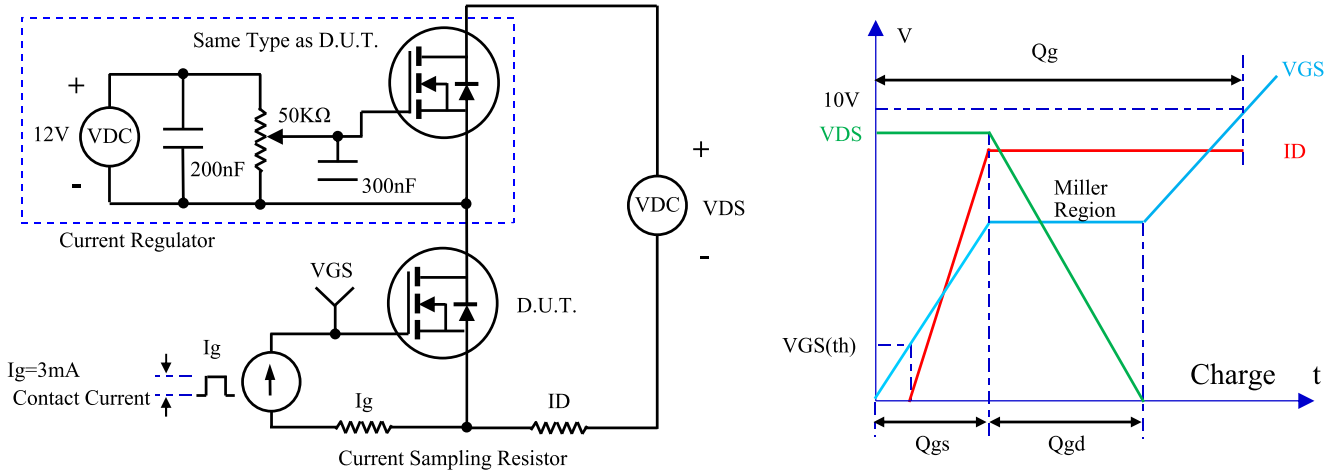


Fig.15- Gate Charge Test Circuit & Waveform

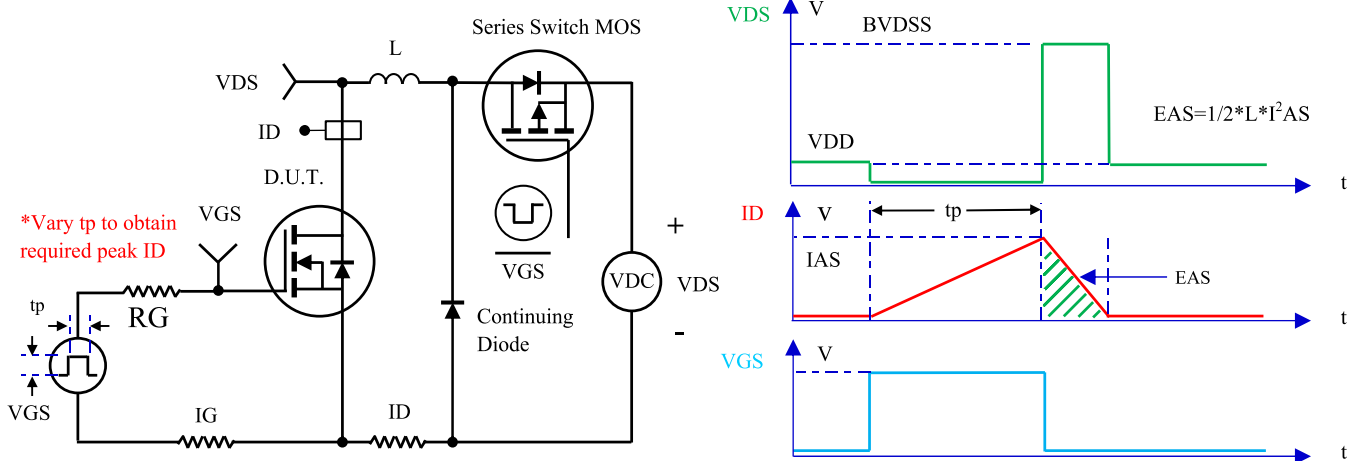


Fig.16- Unclamped Inductive Switching (UIS) Test Circuit & Waveform

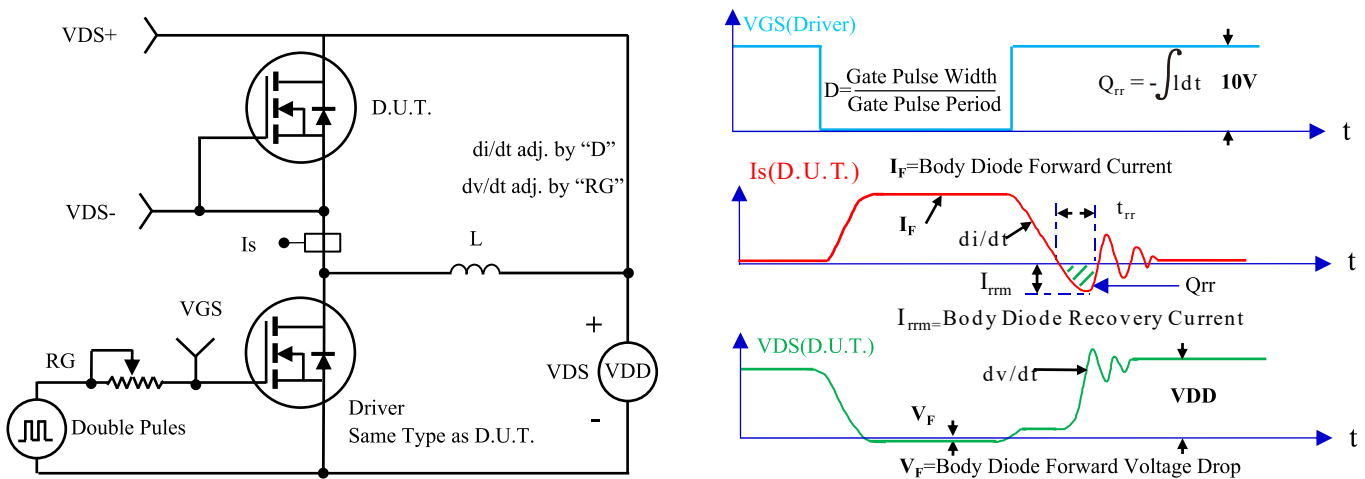


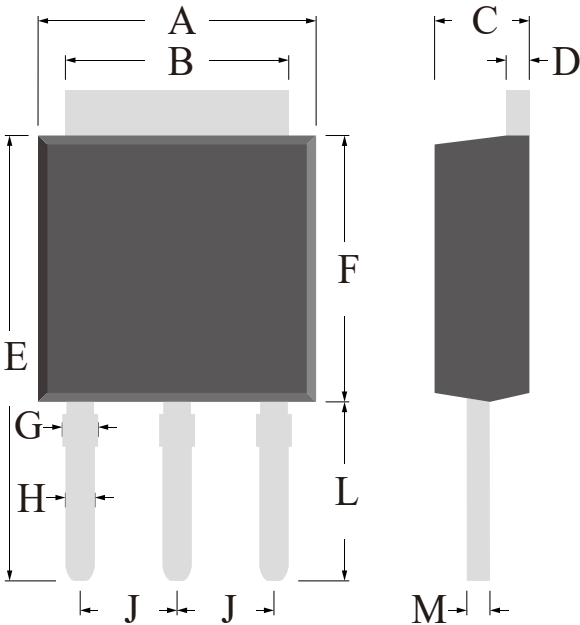
Fig.17- Diode Recovery Test Circuit & Waveform

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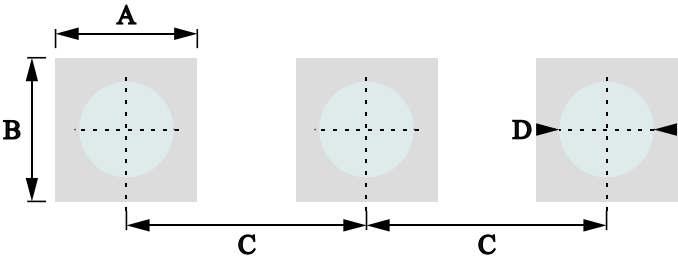
OUTLINE DRAWINGS



TO-251S

OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	6.10	-	7.10	0.2402	-	0.2795
B	4.80	-	5.80	0.1890	-	0.2283
C	1.95	-	2.55	0.0768	-	0.1004
D	0.35	-	0.75	0.0138	-	0.0295
E	10.50	-	12.00	0.4134	-	0.4724
F	5.60	-	6.60	0.2205	-	0.2598
G	0.70	-	1.00	0.0276	-	0.0394
H	0.50	-	0.90	0.0197	-	0.0354
J	2.10	-	2.50	0.0827	-	0.0984
L	3.80	-	4.40	0.1496	-	0.1732
M	0.35	-	0.75	0.0138	-	0.0295

RECOMMEDND LAYOUT DRAWINGS



TO-251S

OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	1.60	-	-	0.0630	-
B	-	1.60	-	-	0.0630	-
C	-	2.30	-	-	0.0906	-
D	-	0.90	-	-	0.0354	-

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MARKING INFORMATION



MARKING INSTRUCTIONS

NH=Niuhang Trademark
FF=Product Line Code,According To Actual Changes
YWW=Date Code,According To Actual Changes
LLWWF=Inernal Code,According To Actual Changes
NPS4N70ES=Model

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
TO-251S	P1	0.324	Tube	50	4000	20000

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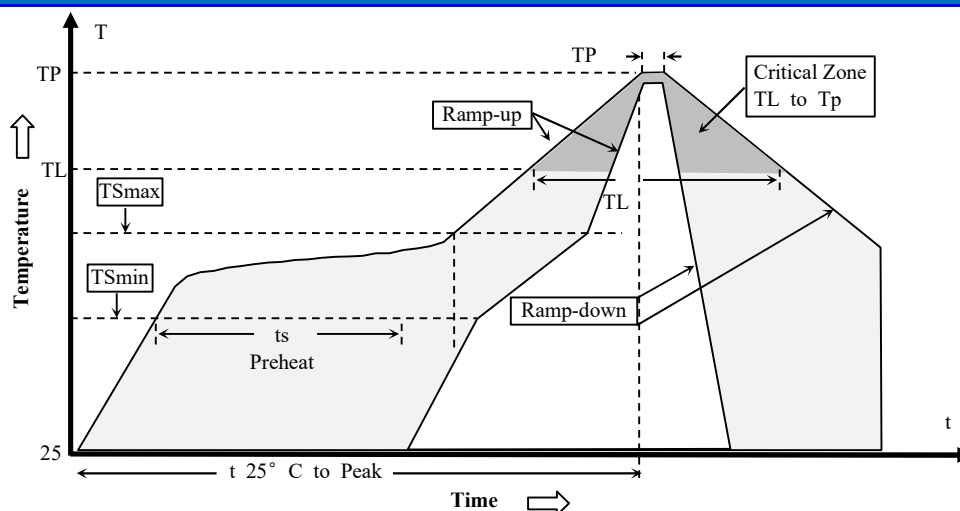
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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(T _S min) -Temperature Max(T _S max) -Time(t _s min to t _s max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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