

PTL 6205



N-Channel Enhancement Mode Trench Power MOSFET

Voltage: 60 Volts

Current: 5.0 Ampers

Package: SOT-23

Features

- NH'S Advanced Trench Technology
- Low Rds(on) For Low On-State Loss
- High EAS For High Reliability
- Excellent Qg*Rds(on) Product(FOM)

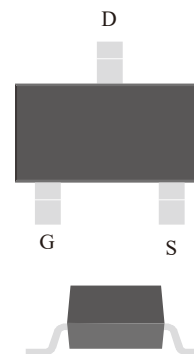
Typical Applications

- DC-DC Converter
- Battery Management System(BMS)
- Printed Circuit Board For Control Circuit
- Uninterruptible Power Supplies(UPS)

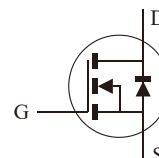
Product Summary

VDS Min.@Tj	60	V
Id Min.@Ta	5.0	A
RDS(ON)(TYP)@10V	26.5	mΩ

Diagram:



Polarity:



***100% UIS TESTED**

***100% ΔVDS TESTED**

Absolute Maximum Ratings (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current (Note 1)	Ta= 25 ℃	I _D	5.0	A
	Ta= 100 ℃		3.2	
Drain Current-Pulse (Note 1)	Tj< 150 ℃	I _{DM}	20	A
Maximum Power Dissipation Power Dissipation Derating Factor Above 25℃	Ta= 25 ℃	P _D	2	W
	Ta= 100 ℃		0.8	
Derating Factor		D _F	0.02	W/℃

Thermal Characteristics (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Junction Temperature		T _J	-55 to 150	℃
Storage Temperature Range		T _{STD}	-55 to 150	℃
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25℃	R _{θJA}	62.5	℃/W

Notes: 1.Pulse Width Limited By Max. Junction Temperature. (See Fig. 13).

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Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static Off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	BV _{DSS}	60	--	--	V
Bvdss Temperature Coefficient	ID=250uA,Reference25℃	ΔBV _{DSS} /ΔT _J	--	0.07	--	V/℃
Drain-Source Leakage Current	VDS= 60 V,VGS=0V	I _{DSS}	--	--	1	uA
Gate-Body Leakage Current	VGS= ±20 V,VDS=0V	I _{GSS}	--	--	±100	nA
Static On Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	V _{GS(TH)}	1.0	1.5	2.5	V
Drain-Source On Resistance	ID= 2.5 A,VGS= 10.0 V	R _{DS(ON)}	--	26.5	35.0	mΩ
	ID= 2.5 A,VGS= 4.5 V		--	31.8	49.0	
Dynamic Characteristics						
Input Capacitance	VDS= 30 V	C _{iss}	--	1148.0	--	pF
Output Capacitance	VGS= 0 V	C _{oss}	--	58.5	--	pF
Reverse Transfer Capacitance	F= 1 MHZ	C _{rss}	--	49.4	--	pF
Switching Paramters (Test Circuit & Waveform See Fig.14)						
Turn-On Delay Time	VDS= 30 V	t _{d(on)}	--	7.6	--	ns
Turn-On Rise Time	VGS= 10.0 V	t _r	--	20.0	--	ns
Turn-Off Delay Time	RG= 0.0 Ω	t _{d(off)}	--	15.0	--	ns
Turn-Off Rise Time		t _f	--	24.0	--	ns
Gate Charge Paramters (Test Circuit & Waveform See Fig.15)						
Total Gate Charge	VDS= 30 V	Q _g	--	20.3	--	nC
Gate-Source Charge	VGS= 10.0 V	Q _{gs}	--	3.7	--	nC
Gate-Drain Charge	ID= 2.5 A	Q _{gd}	--	5.3	--	nC
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.17)						
Max. Diode Forward Cuurent		I _s	--	--	5.0	A
Max. Pulsed Forward Cuurent		I _{SM}	--	--	17.5	A
Diode Forward Voltage	ID= 2.5 A,VGS=0V	V _{SD}	--	0.80	1.2	V

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Typical Characteristics Curves

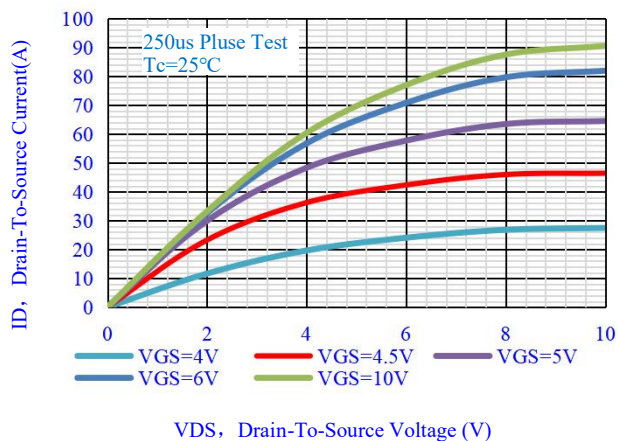


Fig.1-Output Characteristics

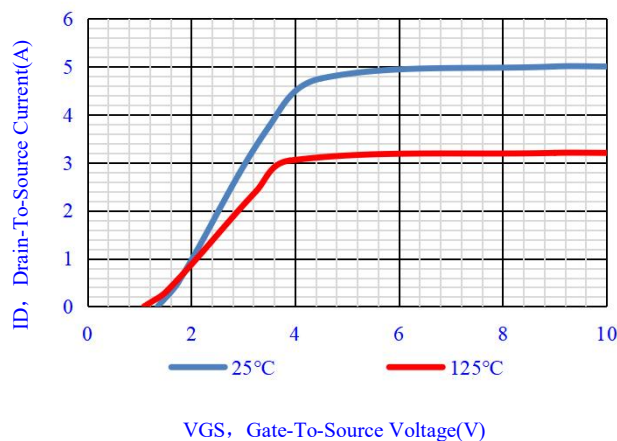


Fig.2- Transfer Characteristics

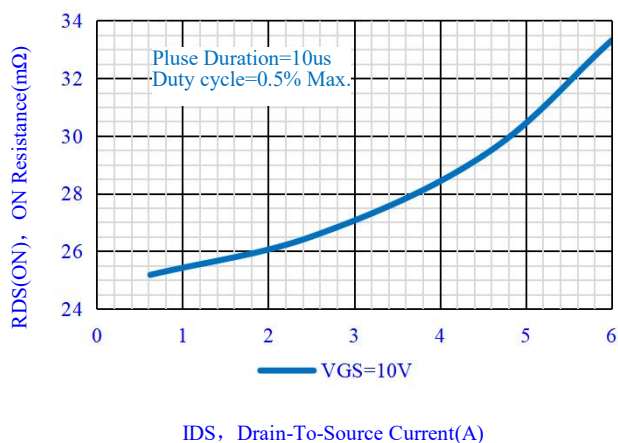


Fig.3- On Resistance vs. Drain Current

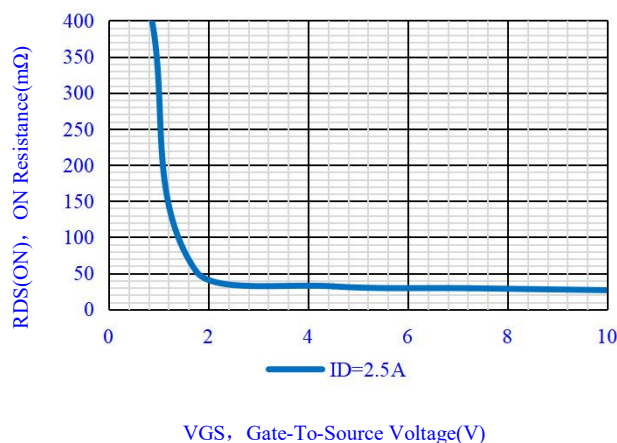


Fig.4- On Resistance vs. Gate Source Voltage

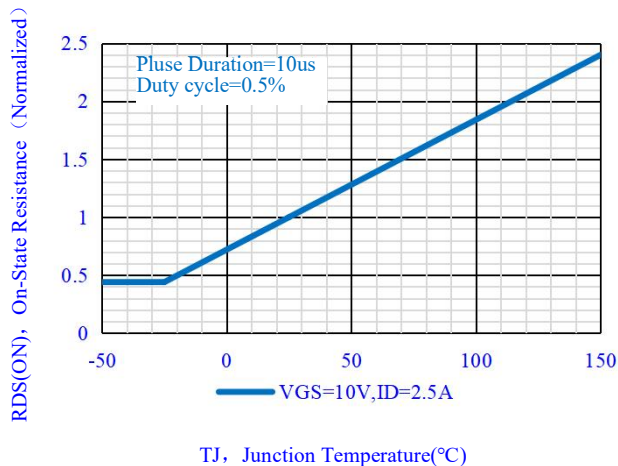


Fig.5- On Resistance vs. Junction Temperature

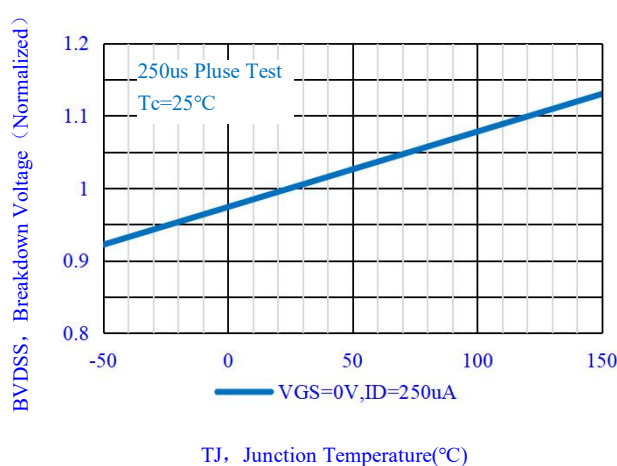


Fig.6- Breakdown Voltage vs. Junction Temperature

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Typical Characteristics Curves

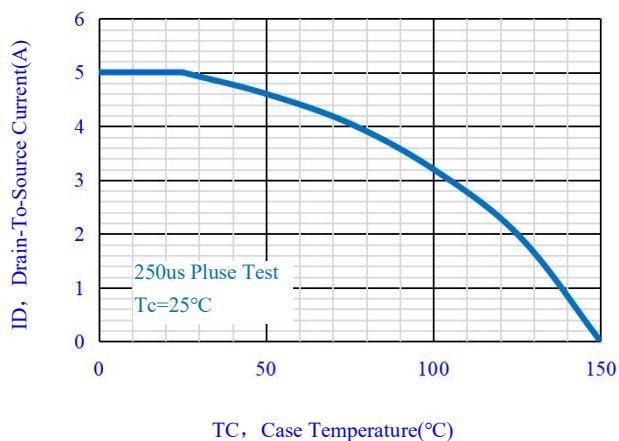


Fig.7-Maximum Continuous Drain Current vs. Case Temperature

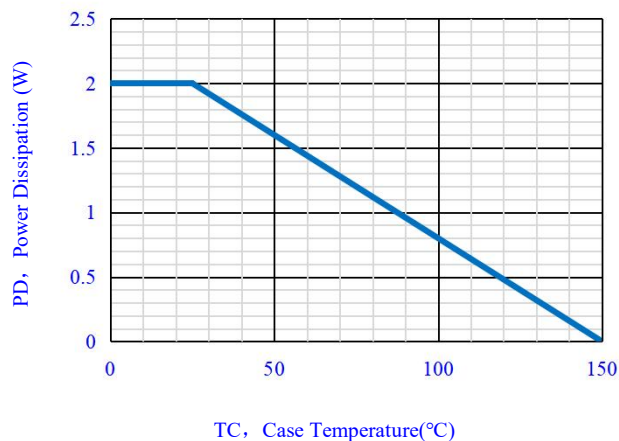


Fig.8-Maximum Power Dissipation vs. Case Temperature

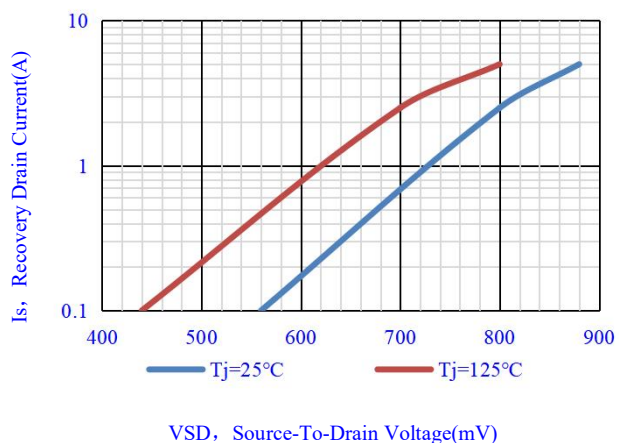


Fig.9- Source-To-Drain Diode Forward Voltage

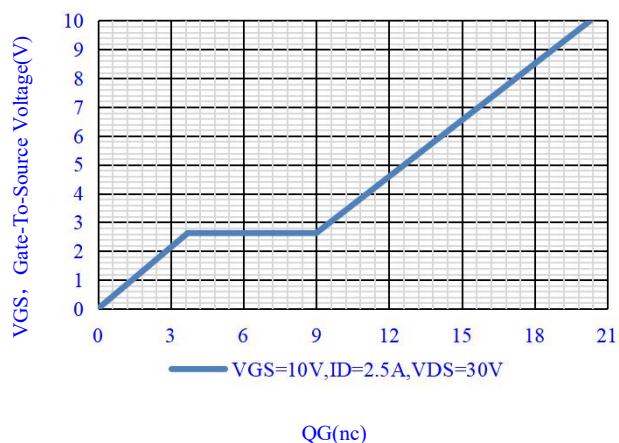


Fig.10-Gate Charge Waveform

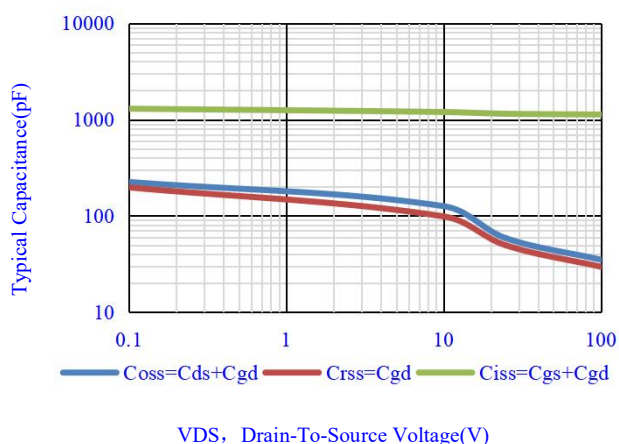


Fig.11-Typical Capacitance vs. Drain-To-Source Voltage

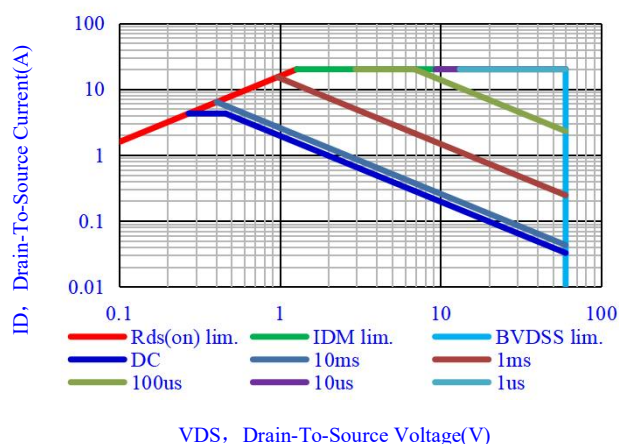


Fig.12-Maximum Safe Operating Area(SOA)

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Typical Characteristics Curves

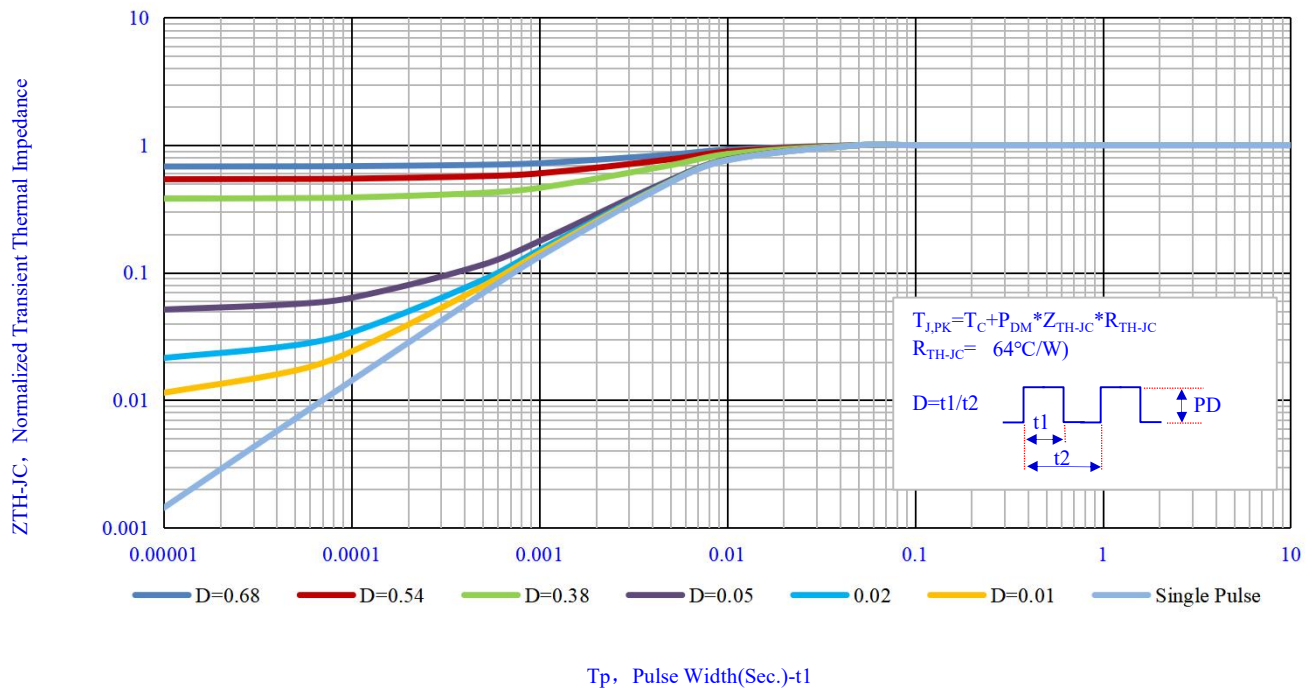


Fig.13- Normalized Maximum Transient Thermal Impedance vs.Pulse Width

Test Circuit & Waveform

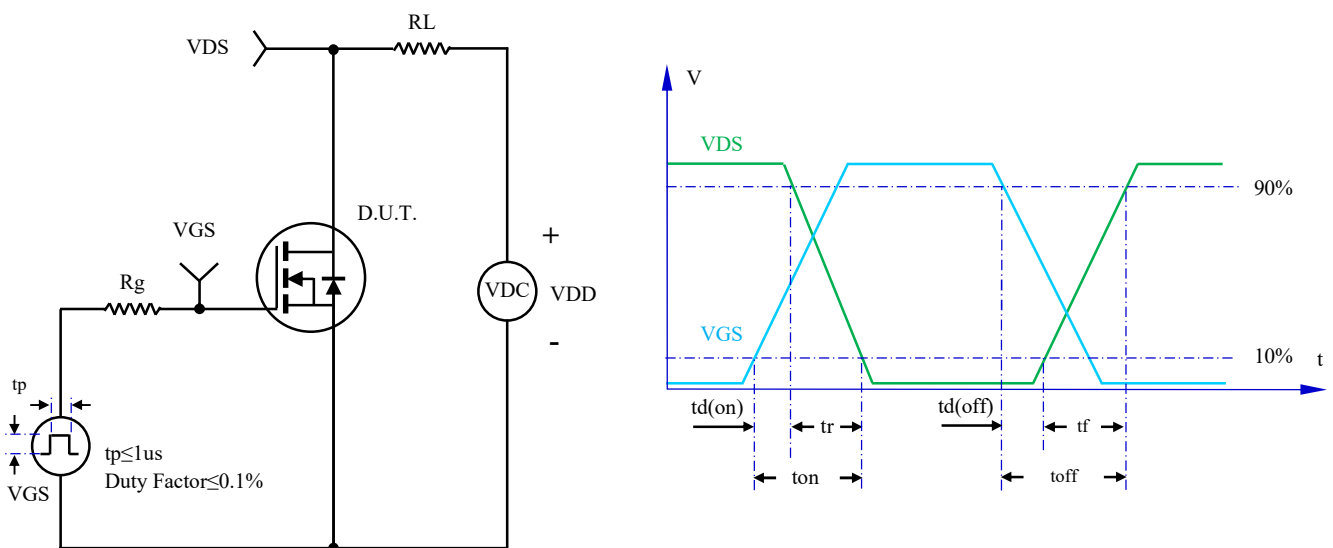


Fig.14- Resistive Switching Test Circuit & Waveform

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Test Circuit & Waveform

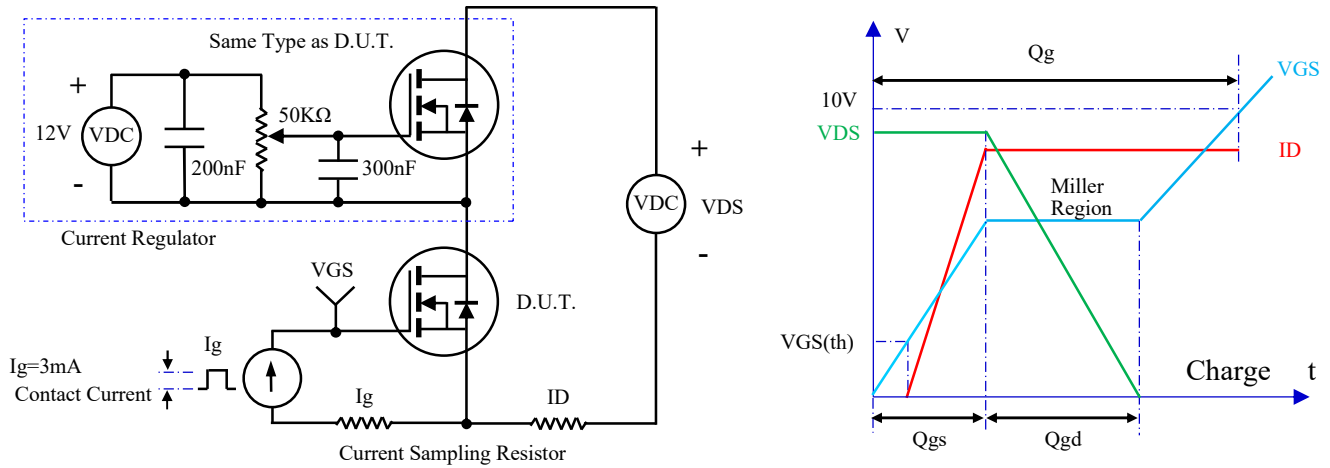


Fig.15- Gate Charge Test Circuit & Waveform

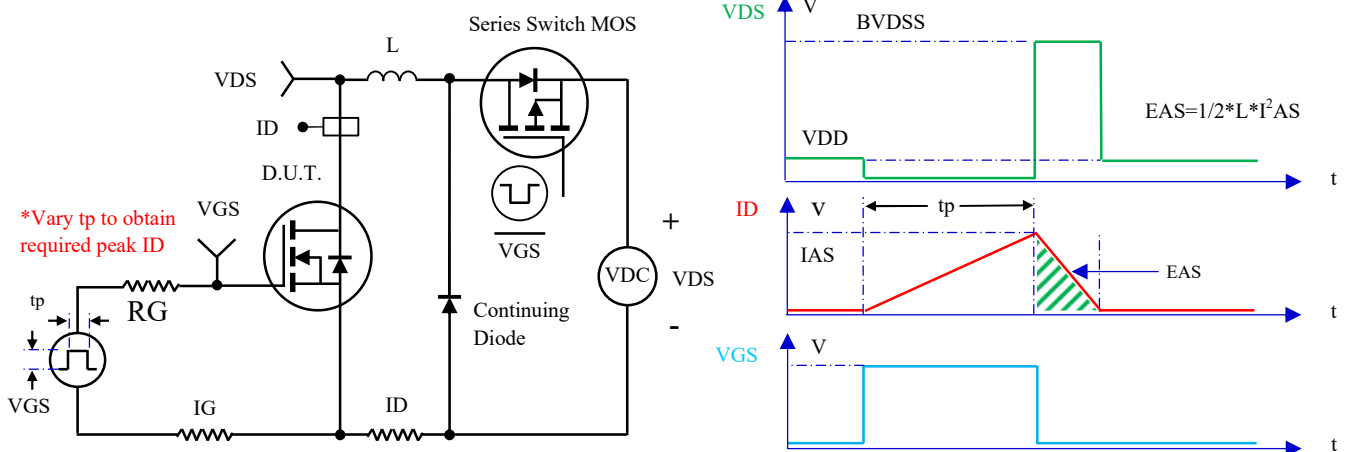


Fig.16- Unclamped Inductive Switching (UIS) Test Circuit & Waveform

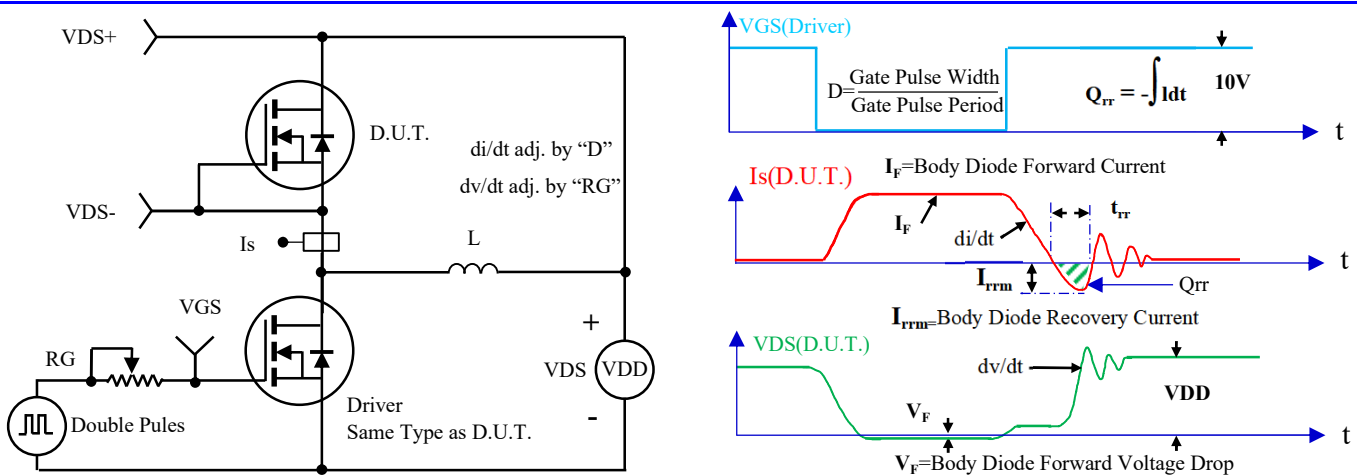


Fig.17- Diode Recovery Test Circuit & Waveform

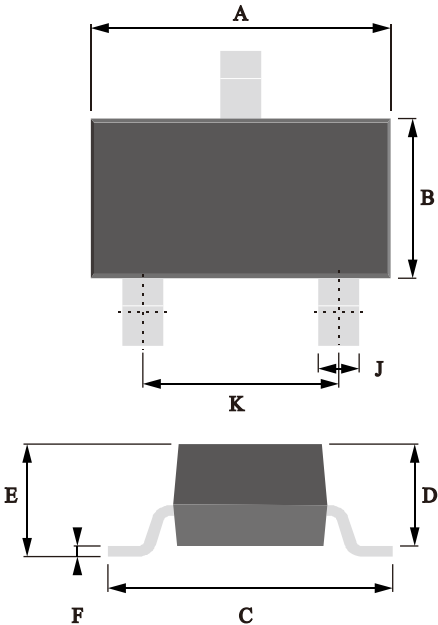
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OUTLINE DRAWINGS

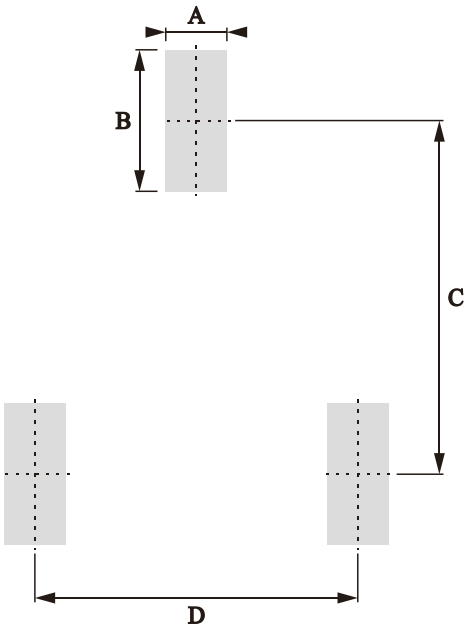
SOT-23



OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.80	-	3.20	0.1102	-	0.1260
B	1.15	-	1.45	0.0453	-	0.0571
C	2.25	-	2.65	0.0886	-	0.1043
D	0.85	-	1.15	0.0335	-	0.0453
E	0.90	-	1.20	0.0354	-	0.0472
F	0.08	-	0.18	0.0031	-	0.0071
J	0.30	-	0.50	0.0118	-	0.0197
K	1.70	-	2.10	0.0669	-	0.0827

RECOMMEDND LAYOUT DRAWINGS

SOT-23



OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	0.60	-	-	0.0000	-
B	-	0.80	-	-	0.0315	-
C	-	2.00	-	-	0.0787	-
D	-	1.90	-	-	0.0748	-

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MARKING INFORMATION



MARKING INSTRUCTIONS

:6205=Make

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
SOT-23	P1	0.008	7" Reel	3000	45000	180000

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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(TS min) -Temperature Max(TS max) -Time(ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (TL) - Time (tL)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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